January 23, 2025 Announcements

Goals

- Intro could - "Armchailt architectum

Review

Approaches to High Performance

- Libraries (seen)
- Black-box Optimizing Compilers
- Compilers with Directives
- Code Transform Systems
- "Active Libraries"
- Q: Give examples of the latter two.

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Libraries: A Case Study



Do Libraries Stand a Chance? (in general)

▶ Tremendously successful approach — Name some examples

Flexibility and composition

(Black-Box) Optimizing Compiler: Challenges

Why is black-box optimizing compilation so difficult?

- Application developer knowledge lost
 - Simple example: "Rough" matrix sizes
 - Data-dependent control flow
 - Data-dependent access patterns
 - Activities of other, possibly concurrent parts of the program
 - Profile-guided optimization can recover some knowledge
- Obtain proofs of required properties

Size of the search space

Consider http://polaris.cs.uiuc.edu/publications/padua.pdf

Directive-Based Compiler: Challenges

What is a directive-based compiler?

- Generally same as optimizing compiler
- Make use of extra promises made by the user
- ► What should the user promise?
- Ideally: feedback cycle between compiler and user
 - Often broken in both directions
 - User may not know what the compiler did
 - Compiler may not be able to express what it needs

Directives: generally not mandatory

prescriptive / descriptive

Lies, Lies Everywhere

- Semantics form a contract between programmer and language/environment
- Within those bounds, implementation has full freedom tor (1) { a Cixy7 = 10
- ► True at every level:
 - Assembly_
 - 'High-level' language (C)

Give examples of lies at these levels:

One approach: Lie to yourself

- ▶ "Domain-specific languages" ← A fresh language, I can do what I want!
- Consistent semantics are notoriously hard to develop
 - Especially as soon as you start allowing subsets of even (e.g.) C's integers

Class Outline

High-level Sections:

- Intro, Armchair-level Computer Architecture
- Machine Abstractions
- ▶ Performance: Expectation, Experiment, Observation
- Programming Languages for Performance
- Program Representation and Optimization Strategies
- Code Generation/JIT

Outline

Introduction

Notes Notes (unfilled, with empty boxes) Notes (source code on Github) About This Class

Why Bother with Parallel Computers?

Lowest Accessible Abstraction: Assembly Architecture of an Execution Pipeline Architecture of a Memory System Shared-Memory Multiprocessors

Machine Abstractions

Performance: Expectation, Experiment, Observation

Parformance Oriented Languages and Abstractions

Moore's Law

Moore's Law: The number of transistors on microchips has doubled every two years

Moore's law describes the empirical regularity that the number of transistors on integrated circuits doubles approximately every two years. This advancement is important for other aspects of technological progress in computing – such as processing speed or the price of computers.



Our World

in Data

Dennard Scaling of MOSFETs

Parameter	Factor
Dimension	$1/\kappa$
Voltage	$1/\kappa$
Current	$1/\kappa$
Capacitance	$1/\kappa$
Delay Time	$1/\kappa$
Power dissipation/circuit	$1/\kappa^2$
Power density	1

[Dennard et al. '74, via Bohr '07]

• Frequency = Delay time⁻¹

MOSFETs ("CMOS" – "complementary" MOS): Schematic



[Dennard et al. '74]